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PRELIMINARY

NLT Technologies, Ltd.

TFT COLOR LCD MODULE

NL13676AC25-01D

39cm (15.6 Type)
FWXGA
LVDS interface (1port)

PRELIMINARY DATA SHEET

DOD-PP-1505 (3rd edition)

www.panelook.com

This PRELIMINARY DATA SHEET is updated document from DOD-PP-1449(2)

All information is subject to change without notice. Please confirm the sales representative before starting to design your system.

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INTRODUCTION

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Examples: Aerospace system (except seat entertainment monitor), nuclear control system, life support system, etc.

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1. OUTLINE

1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL13676AC25-01D is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a signal processing board, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

1.2 APPLICATION

- For industrial use

1.3 FEATURES

- High Contrast
- LED backlight type
- LED driver Built-in
- LVDS interface
- Replaceable lamp holder for backlight

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2. GENERAL SPECIFICATIONS

Display area	344.232 (H) × 193.536 (V) mm	
Diagonal size of display	39.5cm (15.6 inches)	
Drive system	a-Si TFT active matrix	
Display color	16,777,216 colors (At 8-bit input, FRC terminal= Low or Open) 262,144 colors (At 6-bit input, FRC terminal= High)	
Pixel	1366 (H) × 768 (V) pixels	
Pixel arrangement	RGB (Red dot, Green dot, Blue dot) vertical stripe	
Dot pitch	0.084 (H) × 0.252 (V) mm	
Pixel pitch	0.252 (H) × 0.252 (V) mm	
Module size	363.8 mm (W) (typ.) × 215.9 mm (H) (typ.) × (10.3) (D) mm (typ.)	
Weight	(800) g (typ.)	3
Contrast ratio	500:1 (typ.)	
Viewing angle	*At the contrast ratio ≥10:1* • Horizontal: Right side 80° (typ.), Left side 80° (typ.) • Vertical: Up side 80° (typ.), Down side 80° (typ.)	
Designed viewing direction	• Viewing angle with optimum grayscale (γ≒ 2.2): normal axis (perpendicular) • Viewing direction without image reversal: Up side (12 o'clock) • Viewing direction with contrast peak: Down side (6 o'clock)	
Polarizer surface	Anti glare	
Polarizer pencil-hardness	3H (min.) [by JIS K 5600]	
Color gamut	*At LCD panel center* 60% (typ.) [against NTSC color space]	
Response time	*Ton+Toff (10%←→90%)* (18) ms (typ.)	3
Luminance	*At the maximum luminance control* 400 cd/m^2 (typ.)	
Signal system	LVDS 1port	
Power supply voltage	LCD panel: 3.3V LED backlight: 12V	
Backlight	LED backlight type (Replaceable part • Lamp holder set: Type No. 156LHS201)	3
Power consumption	*At the maximum luminance control, Checkered flag pattern* (11.8) W (typ.)	3

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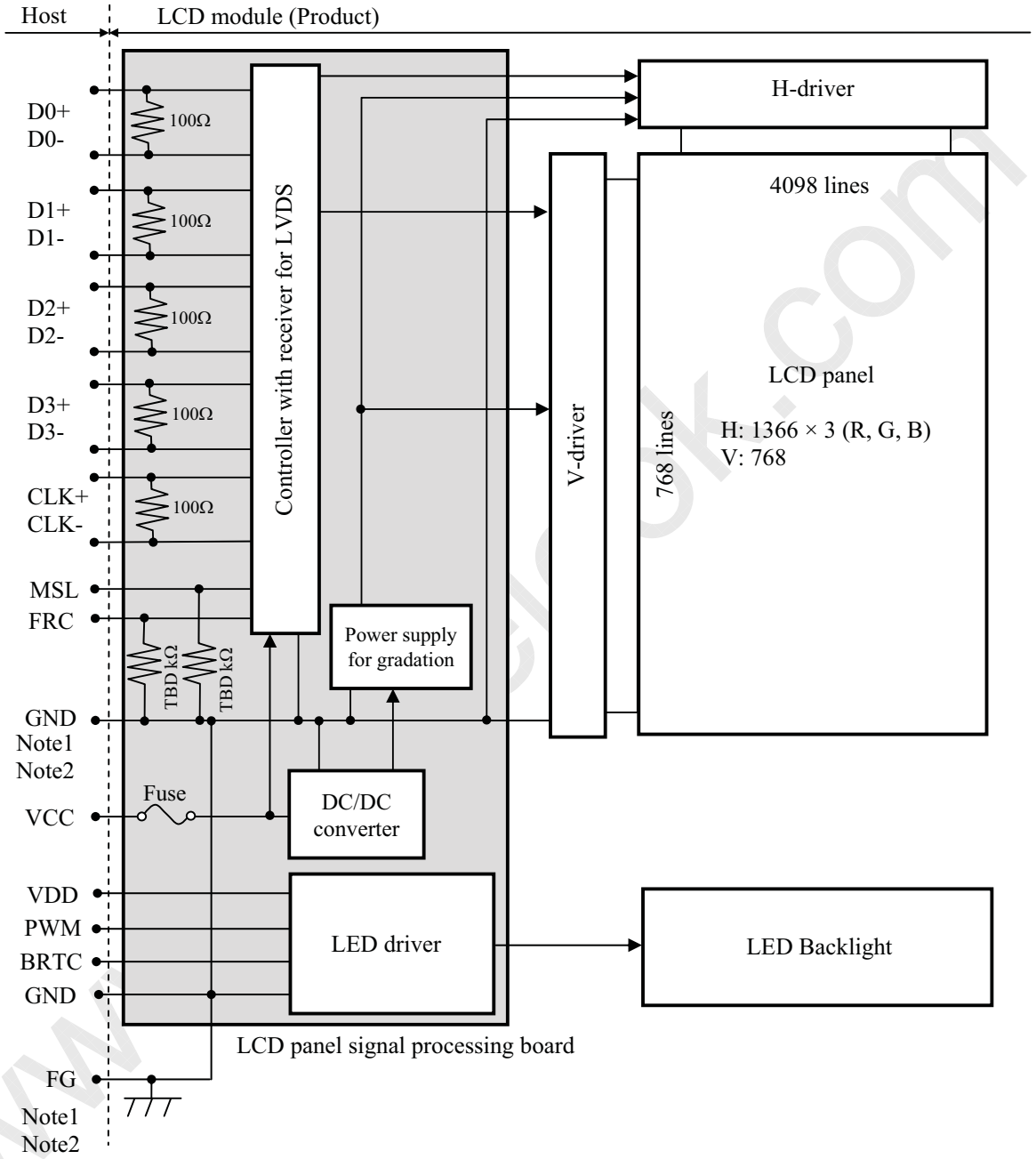
3. BLOCK DIAGRAM



Note1: Relations between GND (Signal ground and LED driver ground) and FG (Frame ground) in the LCD module are as follows.

GND - FG	Connected

Note2: GND and FG must be connected to customer equipment's ground, and it is recommended that these grounds be connected together in customer equipment.

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4. DETAILED SPECIFICATIONS

4.1 MECHANICAL SPECIFICATIONS

Parameter	Specification		Unit
Module size	363.8 ± 0.5 (W) × 215.9 ± 0.5 (H) × (10.3) (D) (typ.)	Note1	mm
Display area	344.232 (H) × 193.536 (V)	Note1	mm
Weight	(800) (typ.), (880) (max.)		g

3

Note1: See "**8. OUTLINE DRAWINGS**".

4.2 ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Rating	Unit	Remarks
Power supply voltage	LCD panel	VCC	-0.3 to +(4.0)	V	Ta= 25°C
	LED driver	VDD	-0.3 to +(25.0)		
Input voltage for signals	Display signals Note1	VD	-0.3 to (+1.98)	V	
	Function signals Note2	VF	-0.3 to VCC		
	Function signal for LED driver	PWM	-0.3 to (+5.5)	V	
		BRTC	-0.3 to (+5.5)	V	
Storage temperature		Tst	-20 to +80	°C	-
Operating temperature	Front surface	TopF	-20 to +70	°C	Note3
	Rear surface	TopR	-20 to +70	°C	Note4
Relative humidity Note5		RH	≤ 95	%	Ta ≤ 40°C
			≤ 85	%	40°C < Ta ≤ 50°C
			≤ 55	%	50°C < Ta ≤ 60°C
			≤ 36	%	60°C < Ta ≤ 70°C
Absolute humidity Note5		AH	≤ 70 Note6	g/m3	Ta > 70°C

Note1: D0+/-, D1+/-, D2+/-, D3+/- and CLK+/-
Note2: FRC and MSL
Note3: Measured at LCD panel surface (including self-heat)
Note4: Measured at LCD module's rear shield surface (including self-heat)
Note5: No condensation.
Note6: Water amount at Ta= 70°C and RH= 36%

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4.3 ELECTRICAL CHARACTERISTICS

4.3.1 LCD panel signal processing board

(Ta = 25°C)

Parameter		Symbol	min.	typ.	max.	Unit	Remarks	
Power supply voltage		VCC	3.0	3.3	3.6	V	-	
Power supply current		ICC	-	(480) Note1	(900) Note2	mA	at VCC= 3.3V	3
Permissible ripple voltage		VRPC	-	-	(300)	mVp-p	for VCC	
Differential input threshold voltage	High	VTH	-	-	+100	mV	at VCM= 1.25V Note3	
	Low	VTL	-100	-	-	mV		
Terminating resistance		RT	-	100	-	Ω	-	
Input voltage for FRC and MSL signals	High	VFH	TBD	-	VCC	V	-	
	Low	VFL	0	-	TBD	V		
Input current for FRC and MSL signals	High	IFH	-	-	TBD	µA		
	Low	IFL	TBD	-	-	µA		

Note1: Checkered flag pattern [by EIAJ ED-2522]
Note2: Pattern for maximum current: TBD
Note3: Common mode voltage for LVDS receiver

4.3.2 Backlight lamp

(Ta=25°C, Note1)

Parameter		Symbol	min.	typ.	max.	Unit	Remarks	
Power supply voltage		VDD	10.8	12.0	13.2	V	Note1	
Power supply current		IDD	-	(850)	(1,100) Note2	mA	At the maximum luminance control. Note3	3
Permissible ripple voltage		VRPD	-	-	(300)	mVp-p	for VDD	
Input voltage for PWM signal	High	VDFH1	1.5	-	(5.5)	V	-	
	Low	VDFL1	0	-	(0.35)	V		3
Input voltage for BRTC signal	High	VDFH2	1.5	-	(5.5)	V	-	
	Low	VDFL2	0	-	0.4	V		
PWM frequency		f_{PWM}	100	-	1k	Hz	Note4, Note5	3
PWM duty cycle			1	-	100	%	Note6, Note7	3
PWM pulse width		tPWH	10	-	-	µs		

Note1: When designing of the power supply, take the measures for the prevention of surge voltage.
Note2: This value excludes peak current such as overshoot current.
Note3: The power supply lines (VDD and GND) may have ripple voltage during luminance control of LED. There is the possibility that the ripple voltage produces acoustic noise and signal wave noise in audio circuit and so on. Put a capacitor between the power supply lines (VDD and GND) to reduce the noise if necessary.

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Note4: A recommended f_{PWM} value is as follows.

$$f_{PWM} = \frac{2n-1}{4} \times fv$$

(n = integer, fv = frame frequency of LCD module)

Note5: Depending on the frequency used, so noise may appear on the screen, please conduct a thorough evaluation.

Note6: While the BRTC signal is high, do not set the tPWH (PWM pulse width) is less than 5μs. It may cause abnormal working of the backlight. In this case, turn the backlight off and then on again by BRTC signal. 3

Note7: Regardless of the PWM frequency, both PWM duty cycle and PWM pulse width must be always more than the minimum values. 3

4.3.3 Power supply voltage ripple

This product works, even if the ripple voltage levels are over the permissible values as the following table, but there might be noise on the display image.

Power supply voltage		Ripple voltage Note1 (Measure at input terminal of power supply)	Unit
VCC	3.3V	≤ (300)	mVp-p
VDD	12.0V	≤ (300)	mVp-p

Note1: The permissible ripple voltage includes spike noise.

4.3.4 Fuse

Parameter	Fuse		Rating	Fusing current	Remarks
	Type	Supplier			
VCC	FCC16152AB	KAMAYA ELECTRIC Co., Ltd.	1.5A 36V	3.0A	Note1
VDD	FCC16202AB	KAMAYA ELECTRIC Co., Ltd.	2.0A 36V	4.0A	

3

Note1: The power supply's rated current must be more than the fusing current. If it is less than the fusing current, the fuse may not blow in a short time, and then nasty smell, smoke and so on may occur.

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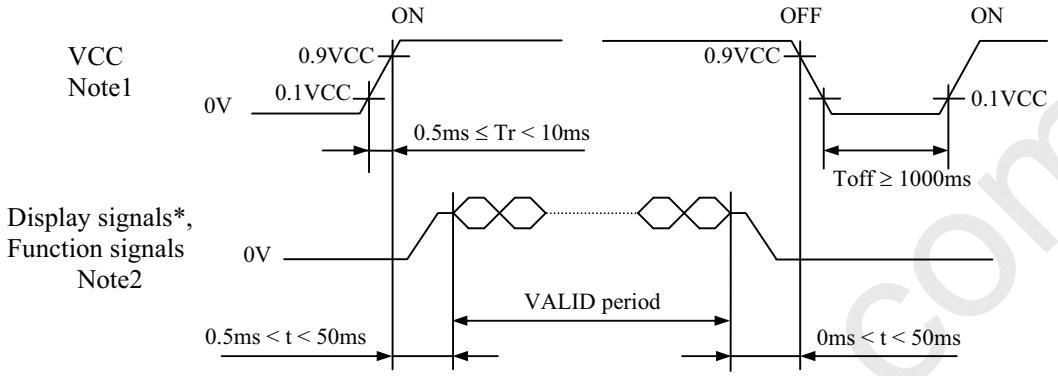
4.4 POWER SUPPLY VOLTAGE SEQUENCE

4.4.1 LCD panel

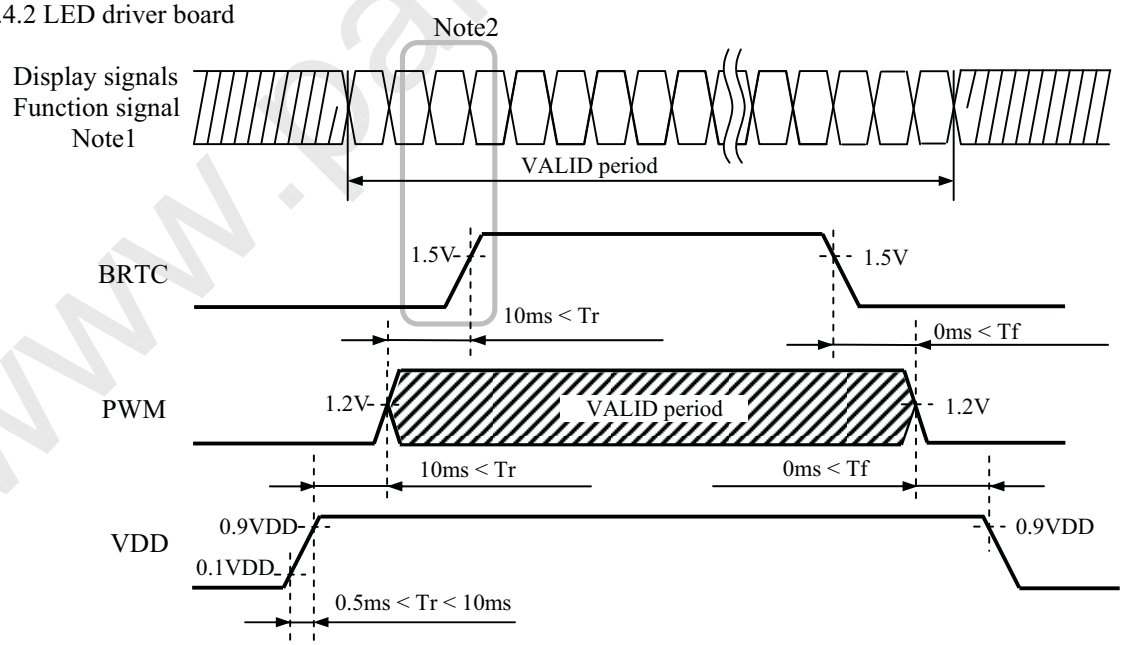


* These signals should be measured at the terminal of 100Ω resistance.

Note1: If there is a voltage variation (voltage drop) at the rising edge of VCC below 3.0V, there is a possibility that a product does not work due to a protection circuit.

Note2: Display signals (D0+/-, D1+/-, D2+/-, D3+/- and CLK+/-) and function signals (FRC, MSL) must be set to Low or High-impedance, except the VALID period (See above sequence diagram), in order to avoid the circuitry damage.
If some of display and function signals of this product are cut while this product is working, even if the signal input to it once again, it might not work normally. If a customer stops the display and function signals, VCC also must be shut down.

4.4.2 LED driver board



Note1: These are the display and function signals for LCD panel signal processing board.

Note2: The backlight should be turned on within the valid period of display and function signals, in order to avoid unstable data display.

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4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

4.5.1 LCD panel signal processing board

CN1 socket (LCD module side): 185083-20121 (P-TWO ELECTRIC TECHNOLOGY CO., LTD.) or equivalent
Adaptable plug: DF14-20S-1.25C (Hirose Electric Co., Ltd. (HRS))

Pin No.	Symbol	Signal	Input data signal: 8bit		Input data signal: 6bit	Remarks
			MAP A	MAP B		
1	VCC	Power supply	Power supply			Note3
2	VCC					
3	GND	Ground	Ground			Note3
4	NC	Non connection	Non connection			
5	D0-	Pixel data	R2-R7,G2	R0-R5,G0		Note2
6	D0+					
7	GND	Ground	Ground			Note3
8	D1-	Pixel data	G3-G7,B2-B3	G1-G5,B0-B1		Note2
9	D1+					
10	GND	Ground	Ground			Note3
11	D2-	Pixel data	B4-B7,DE	B2-B5,DE		Note2
12	D2+					
13	GND	Ground	Ground			Note3
14	CLK-	Pixel clock	Pixel clock			Note2
15	CLK+					
16	GND	Ground	Ground			Note3
17	D3- / GND	Pixel data / Ground	R0-R1, G0-G1, B0-B1	R6-R7, G6-G7, B6-B7	Ground	Note2
18	D3+ / GND					
19	FRC	Selection of the number of colors	Low or Open		High	
20	MSL	Selection of LVDS Input data map	High	Low or Open	High	Note1, Note4

Note1: See "**4.6 DISPLAY COLORS AND INPUT DATA SIGNALS**".
Note2: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.
Note3: All GND and VCC terminals should be used without any non-connected lines.
Note4: See "**4.5.4 Connection between receiver and transmitter for LVDS**".

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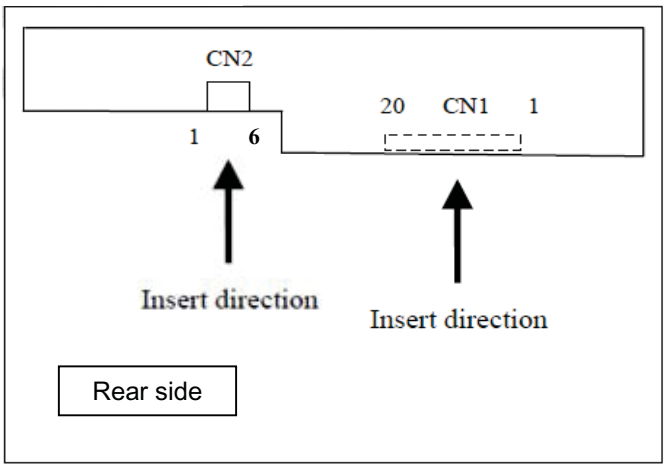
4.5.2 Backlight lamp

CN2 socket (LCD module side): (MSB24038P6 (Produced by STM)) or equivalent.
Adaptable plug: (P24038P6 (Produced by STM)) or equivalent

Pin No.	Symbol	Signal	Remarks
1	VDD	Power supply	Note1
2	VDD	Power supply	Note1
3	GND	Ground	Note1
4	GND	Ground	Note1
5	BRTC	Back light ON/OFF control	High: On, Low: Off
6	PWM	Luminance control	PWM Dimming

Note1: All GND, VCC and VDD terminals should be used without any non-connected lines.

4.5.3 Positions of plug and socket



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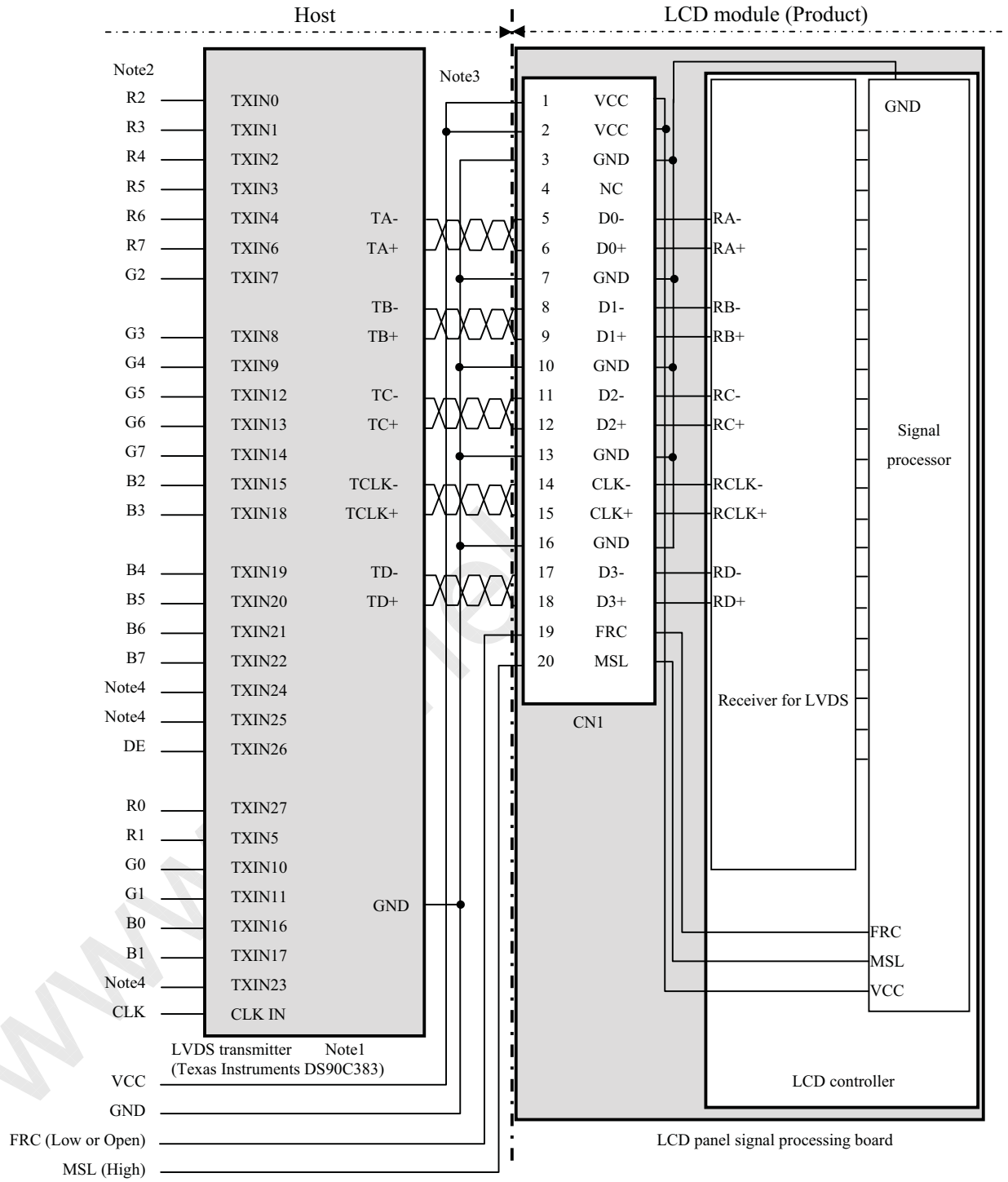
4.5.4 Connection between receiver and transmitter for LVDS
(1) Input data signal: 8bit, MAP A (MSL: High, FRC: Low or Open)



Note1: Recommended transmitter. See the data sheet for DS90C383 (Texas Instruments)
Note2: LSB (Least Significant Bit) - R0, G0, B0 MSB (Most Significant Bit) - R7, G7, B7
Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.
Note4: Input signals to TXIN23, TXIN24 and TXIN25 are not used inside the product, but do not keep TXIN23, TXIN24 and TXIN25 open to avoid noise problem.

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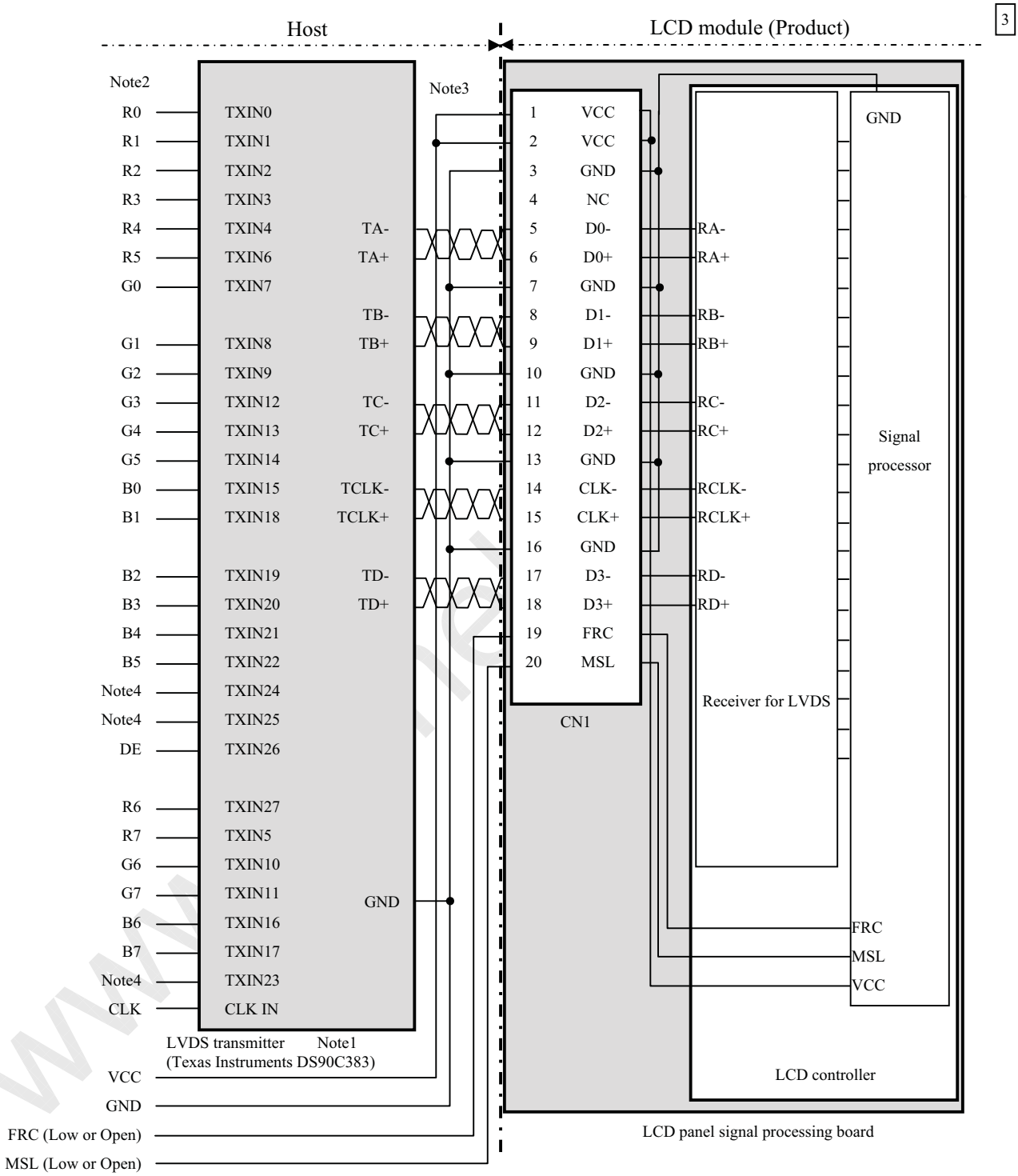
(2) Input data signal: 8bit, MAP B (MSL: Low or Open, FRC: Low or Open)



Note1: Recommended transmitter. See the data sheet for DS90C383 (Texas Instruments).
Note2: LSB (Least Significant Bit) - R0, G0, B0 MSB (Most Significant Bit) - R7, G7, B7
Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.
Note4: Input signals to TXIN23, TXIN24 and TXIN25 are not used inside the product, but do not keep TXIN23, TXIN24 and TXIN25 open to avoid noise problem.

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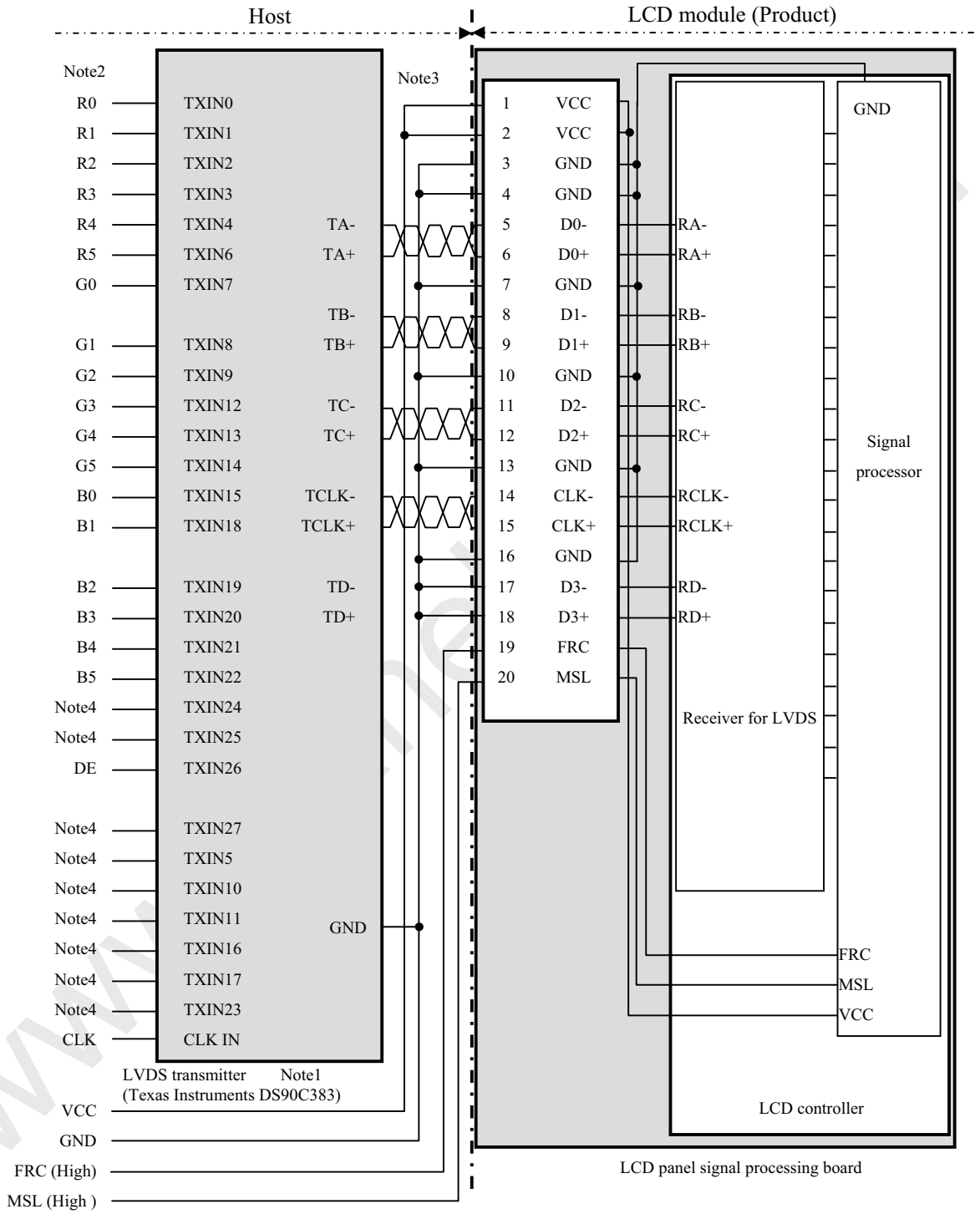
(3) Input data signal: 6bit (MSL: High, FRC: High)



Note1: Recommended transmitter. See the data sheet for DS90C383 (Texas Instruments).

Note2: LSB (Least Significant Bit) - R0, G0, B0 MSB (Most Significant Bit) - R5, G5, B5

Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note4: Input signals to TXIN24, TXIN25, TXIN27, TXIN5, TXIN10, TXIN11, TXIN16, TXIN17 and TXIN23 are not used inside the product, but do not keep TXIN24, TXIN25, TXIN27, TXIN5, TXIN10, TXIN11, TXIN16, TXIN17 and TXIN23 open to avoid noise problem.

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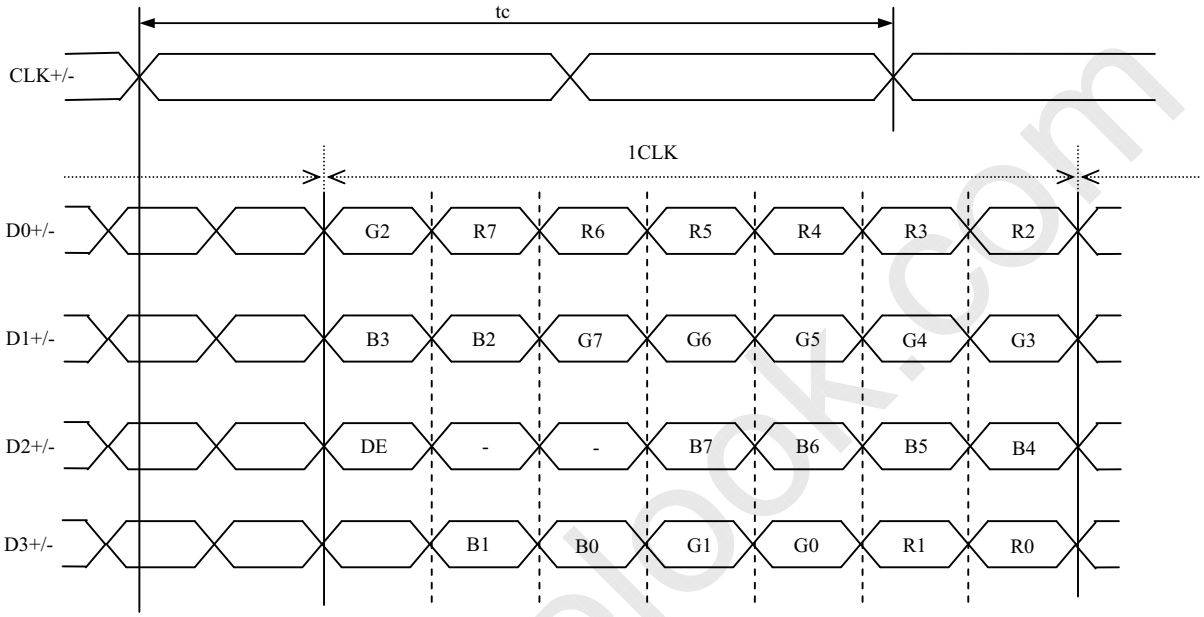
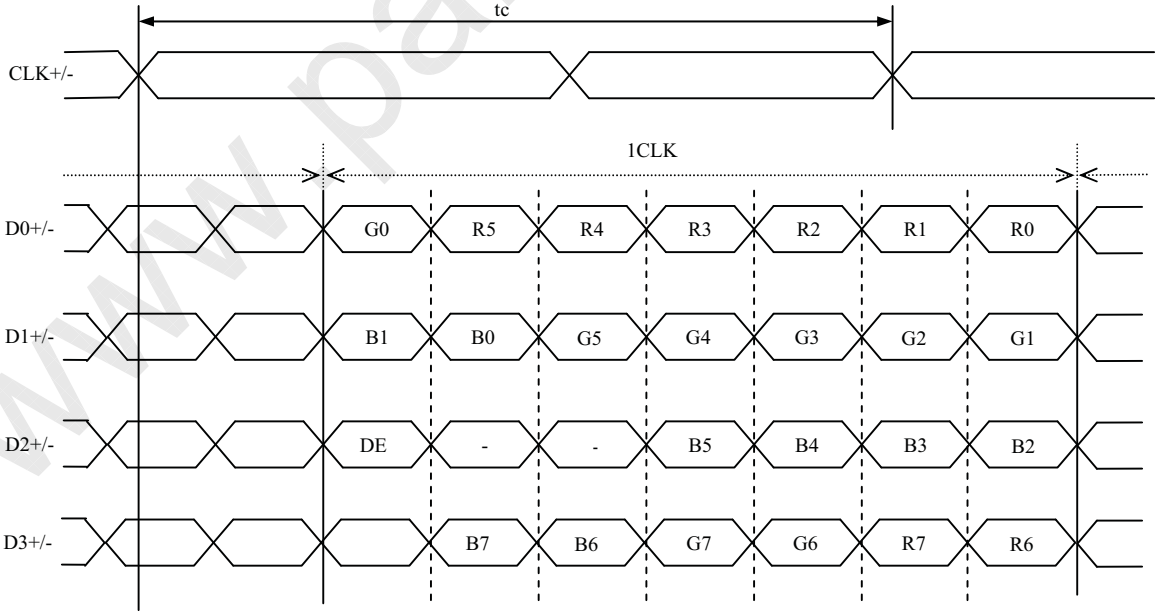
4.5.5 Input data mapping

(1) Input data signal: 8bit, MAP A



(2) Input data signal: 8bit, MAP B



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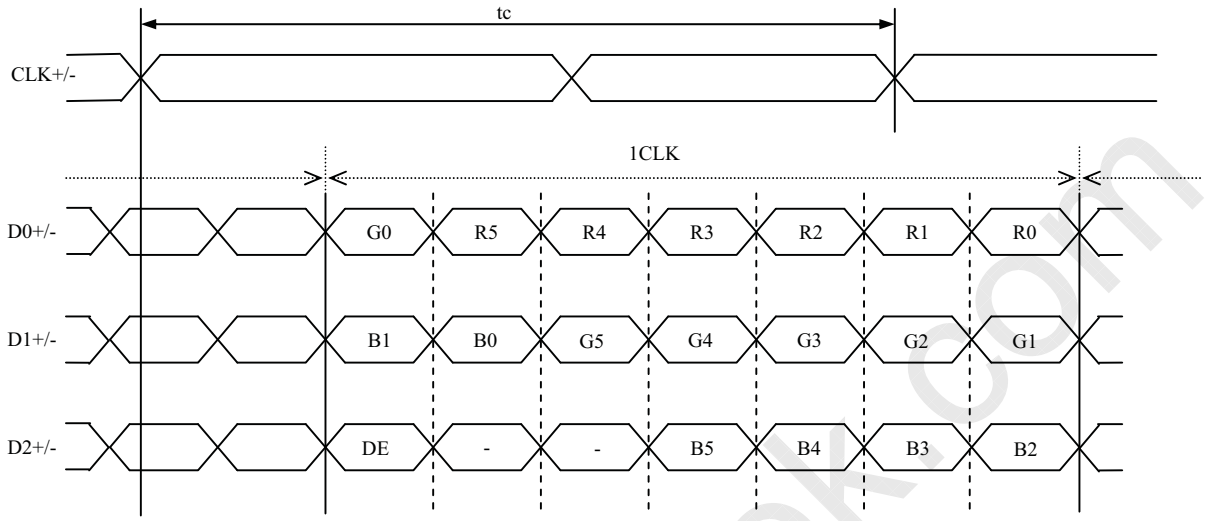
(3) Input data signal: 6bit



4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

4.6.1 Combinations of input data signals, FRC and MSL signal

This product can display 16,777,216 colors equivalent with 256 gray scales and 262,144 colors with 64 gray scales by combination of input data signals, FRC and MSL signal. See the following table.

Combination	Input data signals	Input Data mapping	CN1- Pin No.17 and 18	FRC terminal	MSL terminal	Display colors	Remarks
①	8 bit	MAP A	D3+/-	Low or Open	High	16,777,216	Note1
②	8 bit	MAP B	D3+/-	Low or Open	Low or Open	16,777,216	Note1
③	6 bit	-	GND	High	High	262,144	Note2

Note1: See "**4.6.2 16,777,216 colors**".
Note2: See "**4.6.3 262,144 colors**".

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4.6.2 16,777,216 colors

This product can display 16,777,216 colors equivalent with 256 gray scales by combination ① or ②.
(See "**4.6.1 Combinations of input data signals, FRC and MSL signal**".)
Also the relation between display colors and input data signals is as follows.

Display colors		Data signal (0: Low level, 1: High level)																							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑					:								:								:			
	↓					:								:								:			
	bright	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	↑					:								:								:			
	↓					:								:								:			
	bright	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Blue gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	↑					:								:								:			
	↓					:								:								:			
	bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

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4.6.3 262,144 colors

This product can display 262,144 colors with 64 gray scales by combination ③.
(See "**4.6.1 Combinations of input data signals, FRC and MSL signal**".)
Also the relation between display colors and input data signals is as follows.

Display colors		Data signal (0: Low level, 1: High level)																	
		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	B5	B4	B3	B2	B1	B0
Basic colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑				:						:						:		
	↓				:						:						:		
	bright	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Green gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	↑				:						:						:		
	↓				:						:						:		
	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
		0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Blue gray scale	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	↑				:						:						:		
	↓				:						:						:		
	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1

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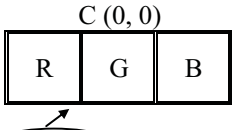
4.7 DISPLAY POSITIONS

The following table is the coordinates per pixel.



C(0, 0)	C(1, 0)	• • •	C(X, 0)	• • •	C(1364, 0)	C(1365, 0)
C(0, 1)	C(1, 1)	• • •	C(X, 1)	• • •	C(1364, 1)	C(1365, 1)
• • •	• • •	• • •	• • •	• • •	• • •	• • •
C(0, Y)	C(1, Y)	• • •	C(X, Y)	• • •	C(1364, Y)	C(1365, Y)
• • •	• • •	• • •	• • •	• • •	• • •	• • •
C(0, 766)	C(1, 766)	• • •	C(X, 766)	• • •	C(1364, 766)	C(1365, 766)
C(0, 767)	C(1, 767)	• • •	C(X, 767)	• • •	C(1364, 767)	C(1365, 767)

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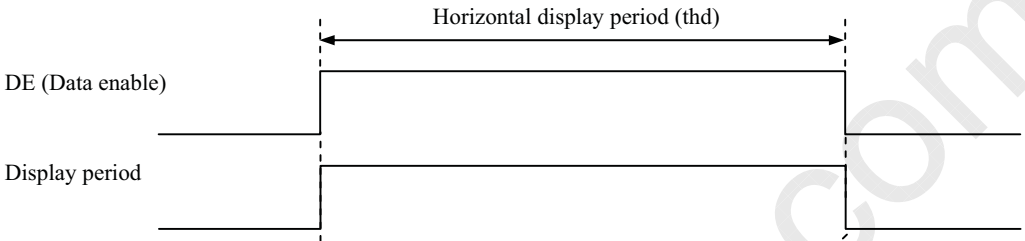
4.8 INPUT SIGNAL TIMINGS

4.8.1 Outline of input signal timings

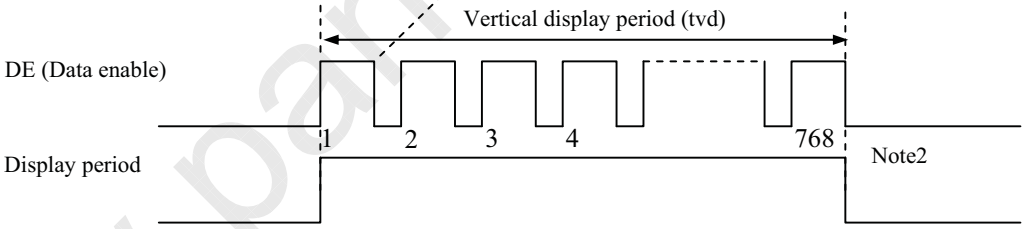
• Horizontal signal

Note1



• Vertical signal

Note1



Note1: This diagram indicates virtual signal for set up to timing.
Note2: See "**4.8.3 Input signal timing chart**" for the pulse number.

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4.8.2 Timing characteristics

(Note1, Note2, Note3)

Parameter			Symbol	min.	typ.	max.	Unit	Remarks
CLK	Frequency		1/tc	(71.0)	75.4	(79.8)	MHz	13.263 ns (typ.)
	Duty		-	-			-	-
	Rise time, Fall time		-				ns	
DATA	CLK-DATA	Setup time	-	-			ns	-
		Hold time	-				ns	
	Rise time, Fall time		-				ns	
DE	Horizontal	Cycle	th	(16.542)	20.678	(26.88)	µs	48.360 kHz (typ.)
				1,446	1,560	1,936	CLK	
		Display period	thd	1366			CLK	-
	Vertical (One frame)	Cycle	tv	(14.29)	16.67	(20.00)	ms	60.0 Hz (typ.)
				778	806	-	H	
		Display period	tvd	768			H	
	CLK-DE	Setup time	-	-			ns	-
		Hold time	-				ns	
	Rise time, Fall time		-				ns	

Note1: Definition of parameters is as follows.
tc= 1CLK, th= 1H
Note2: See the data sheet of LVDS transmitter.
Note3: Vertical cycle (tv) should be specified in integral multiple of Horizontal cycle (th).

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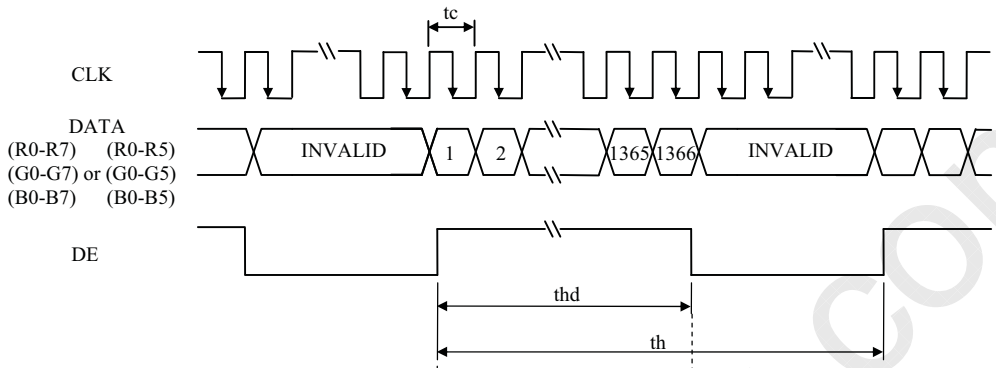
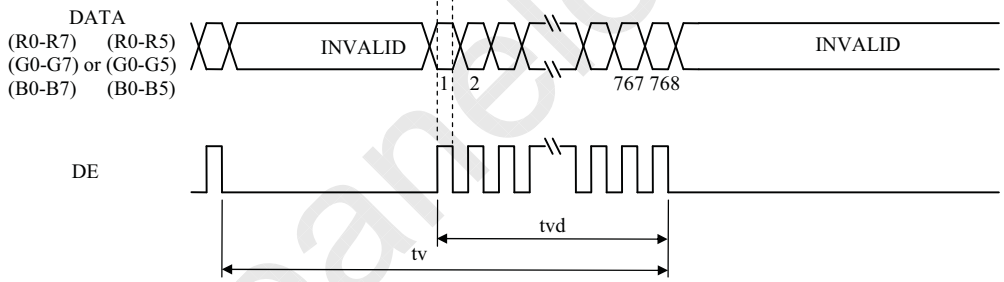
4.8.3 Input signal timing chart

Horizontal timing



Vertical timing



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4.9 OPTICS

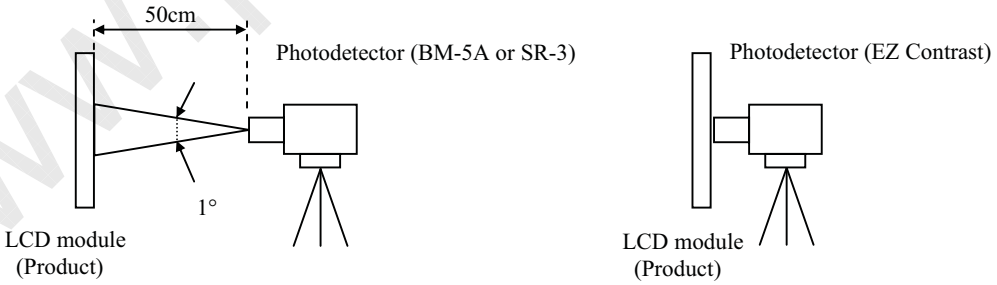
4.9.1 Optical characteristics

(Note1, Note2)

Parameter		Condition	Symbol	min.	typ.	max.	Unit	Measuring instrument	Remarks
Luminance		White at center θR= 0°, θL= 0°, θU= 0°, θD= 0°	L	TBD	400	-	cd/m^2	BM-5A	-
Contrast ratio		White/Black at center θR= 0°, θL= 0°, θU= 0°, θD= 0°	CR	TBD	(500)	-	-	BM-5A	Note3
Luminance uniformity		White θR= 0°, θL= 0°, θU= 0°, θD= 0°	LU	-	1.25	1.4	-	BM-5A	Note4
Chromaticity	White	**x** coordinate	Wx	TBD	0.313	TBD	-	SR-3	Note5
		y coordinate	Wy	TBD	0.329	TBD	-		
	Red	**x** coordinate	Rx	-	TBD	-	-		
		y coordinate	Ry	-	TBD	-	-		
	Green	**x** coordinate	Gx	-	TBD	-	-		
		y coordinate	Gy	-	TBD	-	-		
	Blue	**x** coordinate	Bx	-	TBD	-	-		
		y coordinate	By	-	TBD	-	-		
Color gamut		θR= 0°, θL= 0°, θU= 0°, θD= 0° at center, against NTSC color space	C	-	60	-	%		
Response time		White to Black	Ton	-	(3)	(5)	ms	BM-5A	Note6 Note7
		Black to White	Toff	-	(15)	(21)	ms		
		Ton + Toff	-	-	(18)	(26)	ms		
Viewing angle	Right	θU= 0°, θD= 0°, CR≥ 10	θR	(70)	80	-	°	BM-5A or EZ Contrast	Note8
	Left	θU= 0°, θD= 0°, CR≥ 10	θL	(70)	80	-	°		
	Up	θR= 0°, θL= 0°, CR≥ 10	θU	(70)	80	-	°		
	Down	θR= 0°, θL= 0°, CR≥ 10	θD	(70)	80	-	°		

Note1: These are initial characteristics.
Note2: Measurement conditions are as follows.
Ta= 25°C, VCC= 3.3V, VDD=12.0V, PWM: Duty 100%,
Display mode: FWXGA, Horizontal cycle= 1/48.360kHz, Vertical cycle= 1/60.0Hz,

Optical characteristics are measured at luminance saturation 20minutes after the product works, in the dark room. Also measurement methods are as follows.



Note3: See "**4.9.2 Definition of contrast ratio**".
Note4: See "**4.9.3 Definition of luminance uniformity**".
Note5: These coordinates are found on CIE 1931 chromaticity diagram.
Note6: Product surface temperature: TopF= TBD°C
Note7: See "**4.9.4 Definition of response times**".
Note8: See "**4.9.5 Definition of viewing angles**".

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4.9.2 Definition of contrast ratio

The contrast ratio is calculated by using the following formula.

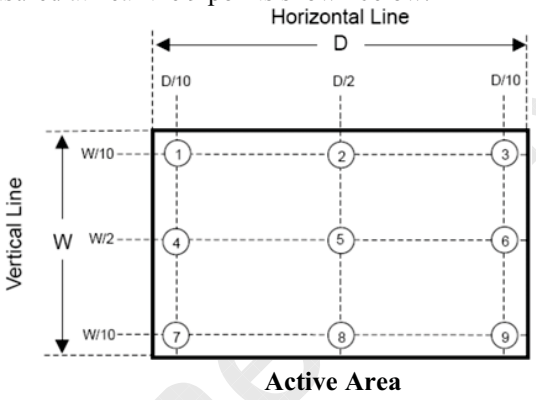
$$\text{Contrast ratio (CR)} = \frac{\text{Luminance of white screen}}{\text{Luminance of black screen}}$$

4.9.3 Definition of luminance uniformity

The luminance uniformity is calculated by using following formula.

$$\text{Luminance uniformity (LU)} = \frac{\text{Maximum luminance from ① to ⑨}}{\text{Minimum luminance from ① to ⑨}}$$

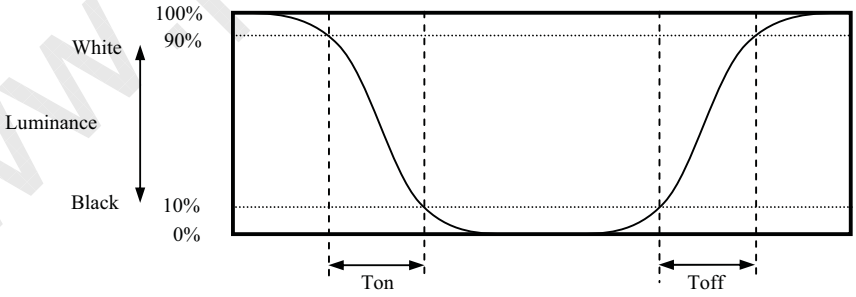
The luminance is measured at near the 9 points shown below.



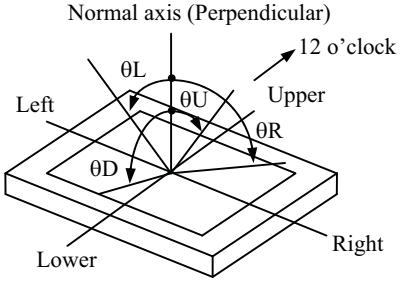
Active Area

4.9.4 Definition of response times

Response time is measured at the time when the luminance changes from " white " to " black ", or " black " to " white " on the same screen point, by photo-detector. Ton is the time when the luminance changes from 90% down to 10%. Also Toff is the time when the luminance changes from 10% up to 90% (See the following diagram.).



4.9.5 Definition of viewing angles



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5. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

This lifetime is the estimated value, and is not guarantee value.

Condition		Estimated luminance lifetime (Life time expectancy) Note1, Note2, Note3	Unit
LED elementary substance	25°C (Ambient temperature of the product) Continuous operation, PWM Duty:100%	70,000	h

Note1: Life time expectancy is mean time to half-luminance.
Note2: Estimated luminance lifetime is not the value for an LCD module but the value for LED elementary substance.
Note3: By ambient temperature, the lifetime changes particularly. Especially, in case the product works under high temperature environment, the lifetime becomes short.

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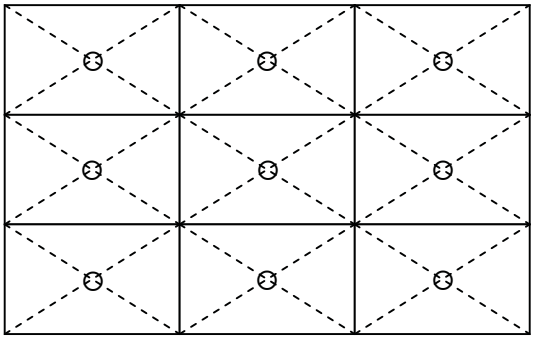
6. RELIABILITY TESTS

Test item	Condition	Judgment Note1
High temperature and humidity (Operation)	① 60 ± 2°C, RH= 90%, 240hours ② Display data is black.	No display malfunctions
High temperature (Operation)	① 70 ± 3°C, 240hours ② Display data is black.	
Heat cycle (Operation)	① -20 ± 3°C…1hour 70 ± 3°C…1hour ② 50cycles, 4 hours/cycle ③ Display data is Black.	
Thermal shock (Non operation)	① -20 ± 3°C…30minutes 80 ± 3°C…30minutes ② 100cycles, 1hour/cycle ③ Temperature transition time is within 5 minutes.	
ESD (Operation)	① 150pF, 150Ω, ±10kV ② 9 places on a panel surface Note2 ③ 10 times each places at 1 sec interval	
Dust (Operation)	① Sample dust: No. 15 (by JIS-Z8901) ② 15 seconds stir ③ 8 times repeat at 1 hour interval	
Vibration (Non operation)	① 5 to 100Hz, 11.76m/s^2 ② 1 minute/cycle ③ X, Y, Z directions ④ 50 times each directions	No display malfunctions No physical damages
Mechanical shock (Non operation)	① 294m/s^2, 11ms ② X, Y, Z directions ③ 3 times each directions	

Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect criteria.
Note2: See the following figure for discharge points.



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7. PRECAUTIONS

7.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. **Be sure to read "7.2 CAUTIONS" and "7.3 ATTENTIONS"!**

This sign has the meaning that a customer will be injured or the product will sustain damage if the customer practices wrong operations.

This sign has the meaning that a customer will be injured if the customer practices wrong operations.

7.2 CAUTIONS

*** Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: Equal to or no greater than 294m/s^2 and equal to or no greater than 11ms, Pressure: Equal to or no greater than 19.6 N (ϕ16mm jig))**

3

7.3 ATTENTIONS

7.3.1 Handling of the product

① Take hold of both ends without touching the circuit board when the product (LCD module) is picked up from inner packing box to avoid broken down or misadjustment, because of stress to mounting parts on the circuit board.
② When the product is put on the table temporarily, display surface must be placed downward.
③ When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
④ The torque for product mounting screws must never exceed 0.34N·m. Higher torque might result in distortion of the bezel. And the length of product mounting screws must be ≤ TBDmm.
⑤ The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings). And do not add undue stress to any portion (such as bezel flat area). Bends or twist described above and undue stress to any portion may cause display mura.
⑥ Do not press or rub on the sensitive product surface. When cleaning the panel surface, wipe it with a soft dry cloth.
⑦ Do not push or pull the interface connectors while the product is working. When handling the product, use of an original protection sheet on the product surface (polarizer) is recommended for protection of product surface. Adhesive type protection sheet may change color or characteristics of the polarizer.
⑧ Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal by any chance, please wash it away with soap and water.

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7.3.2 Environment

① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
② In order to prevent dew condensation occurred by temperature difference, the product packing box must be opened after enough time being left under the environment of an unpacking room. Evaluate the storage time sufficiently because dew condensation is affected by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with the original packing state after a customer receives the package)
③ Do not operate in high magnetic field. If not, circuit boards may be broken.
④ This product is not designed as radiation hardened.

7.3.3 Characteristics

The following items are neither defects nor failures.

① Characteristics of the LCD (such as response time, luminance, color uniformity and so on) may be changed depending on ambient temperature. If the product is stored under condition of low temperature for a long time, it may cause display mura. In this case, the product should be operated after enough time being left under condition of operating temperature.
② Display mura, flickering, vertical streams or tiny spots may be observed depending on display patterns.
③ Do not display the fixed pattern for a long time because it may cause image sticking. Use a screen saver, if the fixed pattern is displayed on the screen.
④ The display color may be changed depending on viewing angle because of the use of condenser sheet in the backlight.
⑤ Optical characteristics may be changed depending on input signal timings.

7.3.4 Others

① All GND, VCC and VDD terminals should be used without any non-connected lines.
② Do not disassemble a product or adjust variable resistors.
③ See "REPLACEMENT MANUAL FOR LAMP HOLDER SET", when replacing lamp holder set.
④ Pack the product with the original shipping package, in order to avoid any damages during transportation, when returning the product to NLT for repairing and so on.

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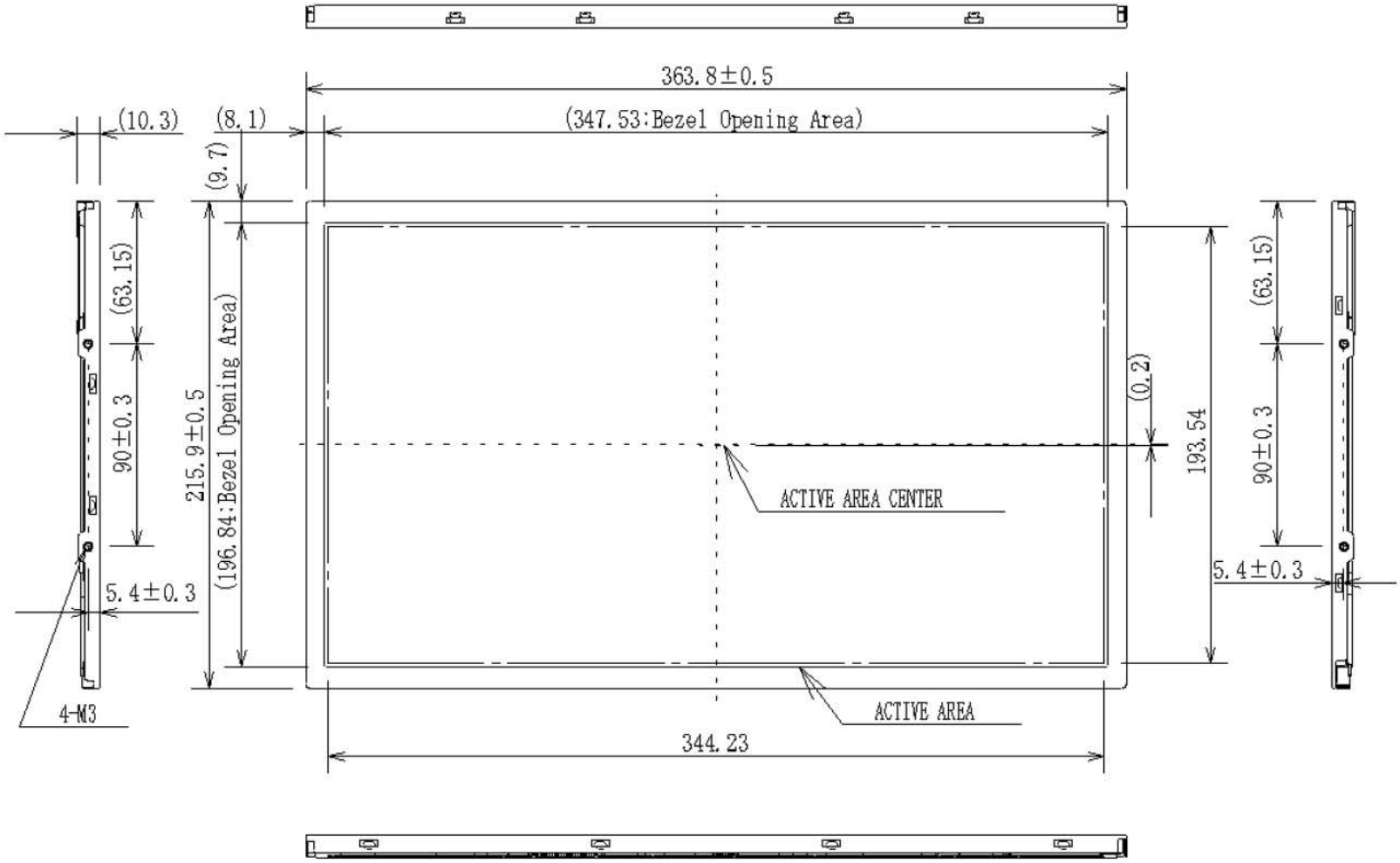
PRELIMINARY

8. OUTLINE DRAWINGS

8.1 FRONT VIEW



Note1: The values in parentheses are for reference.
Note2: The torque for product mounting screws must never exceed 0.34N·m.

Unit: mm

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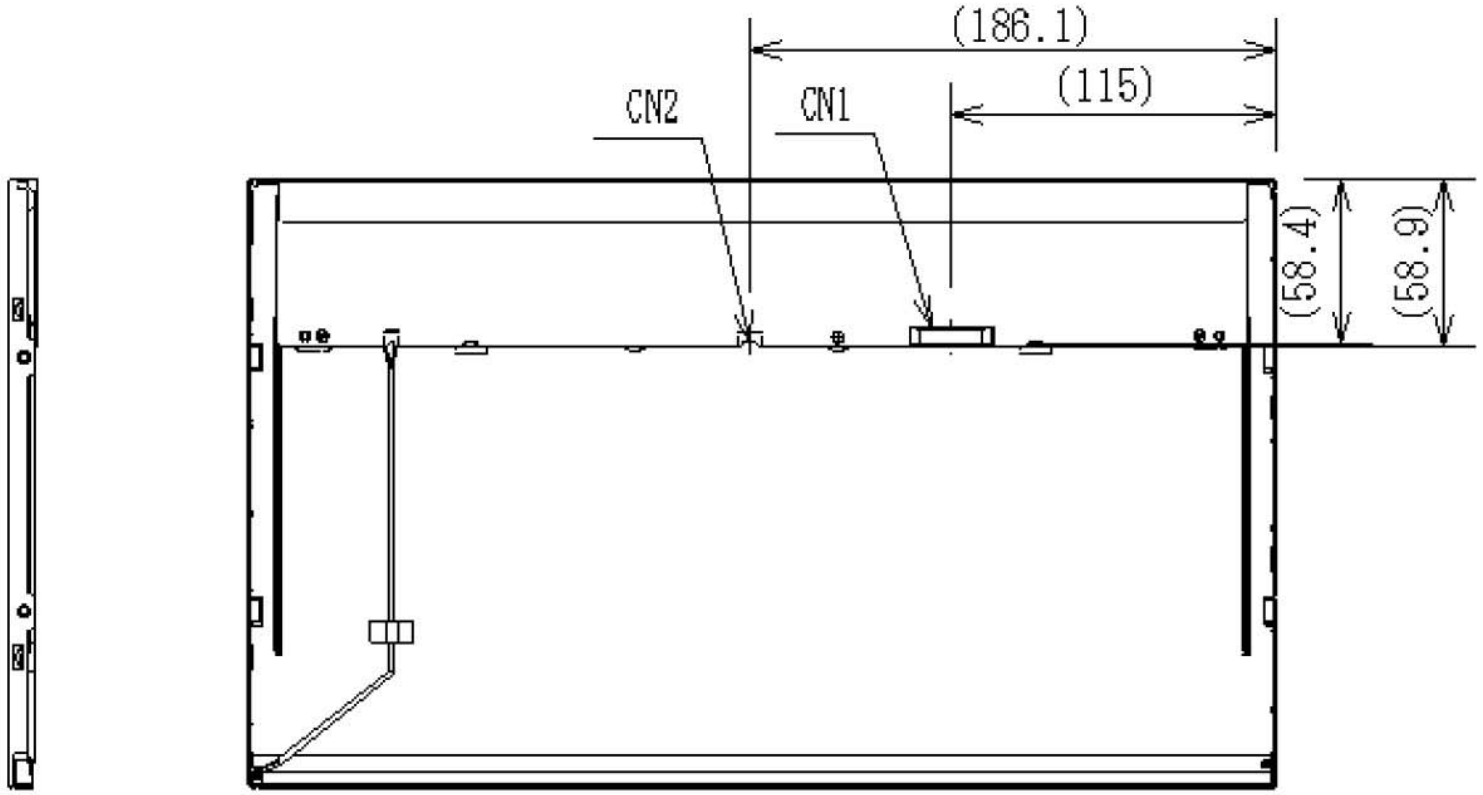
8.2 REAR VIEW



Note1: The values in parentheses are for reference.
Note2: The torque for product mounting screws must never exceed 0.34N·m.

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PRELIMINARY

REVISION HISTORY

The inside of latest specifications is revised to the clerical error and the major improvement of previous edition. Only a changed part such as functions, characteristic value and so on that may affect a design of customers, are described especially below.

Edition	Document number	Prepared date	Revision contents and signature
1st edition	DOD-PP-1378	Mar. 9, 2012	**Revision contents** New issue **Write** *Approved by* T. OGAWA *Checked by* ——— *Prepared by* T. OGAWA
2nd edition	DOD-PP-1449	Jun. 15, 2012	**Revision contents** P1 NL13676AC25-XX, WXGA → NL13676AC25-01D, FWXGA P5 GENERAL SPECIFICATIONS • Module size: 11.0 (D) mm (typ.) → (10.3) (D) mm (typ.) • Contrast ratio: (500):1 (typ.) → 500:1 (typ.) • Designed viewing direction (addition) • Polarizer pencil-hardness (addition) • Power consumption: Gray pattern → Checkered flag pattern P6 BLOCK DIAGRAM • FRC - VCC → FRC - GND P7 MECHANICAL SPECIFICATIONS • Module size: 11.0 (D) mm (typ.) → (10.3) (D) mm (typ.) P7 ABSOLUTE MAXIMUM RATINGS • Power supply voltage - LCD panel: -0.3 to +6.0 V → -0.3 to +(4.0) V - LED driver: TBD V → -0.3 to +(25.0) V • Input voltage for signals - Display signals: -0.3 to +3.2 V → -0.3 to (+1.98) V - Function signals: -0.3 to +3.2 V →-0.3 to VCC V - Function signal for LED driver: PWM / BRTC: TBD V → specified • Storage temperature: -20 to +70 °C →-20 to +80 °C • Relative humidity: ≤ 90 %, Ta ≤ +40°C →≤ 95 %, Ta ≤ 40°C ≤ 85 %, 40°C < Ta ≤ 50°C (addition) ≤ 55 %, 50°C < Ta ≤ 60°C (addition) ≤ 36 %, 60°C < Ta ≤ 70°C (addition) • Absolute humidity: - Remarks Ta > +50°C → Ta > 70°C • Note2: MSL → FRC and MSL • Note6 (addition) P8 ELECTRICAL CHARACTERISTICS • LCD panel signal processing board • Power supply current: TBD mA (typ.) → (700) mA (typ.) • Permissible ripple voltage: 100 mVp-p (max.) → (300) mVp-p (max) • Input voltage for FRC and MSL signals: High: TBD V (max.) → VCC V (max.) • Note2: All Gray pattern → Pattern for maximum current: TBD P9 Backlight lamp • Power supply voltage: TBD V (min., max.) → 10.8 V (min.), 13.2 V (max.) • Power supply current: TBD mA (typ.), - mA (max.) → - mA (typ.), TBD mA (max.) Note2 (addition) : Remark - Note2 → Note3 • Permissible ripple voltage: TBD mVp-p (max.) → (300) mVp-p (max.) • Input voltage for PWM signal: High: TBD V (min.), - V (max.) → 1.5 V (min.), (5.5) V (max.) : Low: - V (min.), TBD V (max.) → 0 V (min.), 0.4 V (max.) • Input voltage for BRTC signal: High: TBD V (min., max.) → 1.5 V (min.), (5.5) V (max.) : Low: TBD V (max.) → 0.4 V (max.)

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REVISION HISTORY

Edition	Document number	Prepared date	Revision contents and signature
2nd edition	DOD-PP-1449	Jun. 15, 2012	**Revision contents** P9 Backlight lamp • PWM frequency: TBD Hz (min.), 20k Hz (max.) → 100 Hz (min.), 10k Hz (max.) • Note2 (addition) P9 Power supply voltage ripple • VCC 3.3 V: ≤ 100 mVp-p → ≤ (300) mVp-p • VDD 12.0 V: TBD mVp-p → ≤ (300) mVp-p P10 POWER SUPPLY VOLTAGE SEQUENCE • LCD panel •Note2: function signals (MSL) → function signals (FRC,MSL) P10 LED driver board: VDD (addition) P11 LCD panel signal processing board • CN1 socket (LCD module side): DF14H-20P-1.25H (Hirose Electric Co., Ltd. (HRS)) or equivalent → 185083-20121 (P-TWO ELECTRIC TECHNOLOGY CO., LTD.) or equivalent • Adaptable plug: DF14-20S-1.25C (Hirose Electric Co., Ltd. (HRS)) (addition) • Pin No.4: Symbol- DPS → N.C. : Signal- Selection of scan direction → Non connection :Input data signal- High: Reverse scan, Low or Open: Normal scan → Non connection :Remark- Note4 (elimination) • Pin No.17: Symbol- D3- → D3- / Ground : Signal- Pixel data → Pixel data / Ground • Pin No.18: Symbol- D3+ → D3+ / Ground : Signal- Pixel data → Pixel data / Ground • Note4: See "4.8 SCANNING DIRECTIONS". (elimination) P12 Backlight lamp • CN2 plug (LCD module side): MSB24038P5 (Produced by STM) or equivalent → CN2 socket (LCD module side): (MSB24038P6 (Produced by STM)) or equivalent • Adaptable plug: (P24038P6 (Produced by STM)) (addition) • Pin No.1: Signal- Power supply (12V) → Power supply, Remark- - → Note1 • Pin No.2: Symbol- GND → VDD :Signal- Ground → Power supply, Remark- - → Note1 • Pin No.3: Symbol- BRTC → GND :Signal- Back light ON/OFF control → Ground :Remark- 5V-On / 0V-Off → Note1 • Pin No.4: Symbol- PWM → GND :Signal- Luminance control → Ground :Remark- PWM Dimming → Note1 • Pin No.5: Symbol- N.C. → BRTC :Signal- Non connection → Back light ON/OFF control :Remark- Keep this pin Open. →High: On, Low: Off • Pin No.6 (addition) P12 Positions of plug and socket (addition) P13 Input data signal: 8bit, MAP A (MSL: High, FRC: Low or Open) (revised) P14 Input data signal: 8bit, MAP B (MSL: Low or Open, FRC: Low or Open) (revised) P15 Input data signal: 6bit (MSL: High, FRC: High) (revised) P22 Timing characteristics • CLK - Frequency: 72.2 MHz (min.), 76.0 MHz (typ.), 79.8 MHz (max.) → (71.0) MHz (min.), 75.4 MHz (typ.), (79.8) MHz (max.) : Remarks- 13.158 ns (typ.) → 13.263 ns (typ.) • DE - Horizontal – Cycle: 16.542 µs (min.), 20.676 µs (typ.), 26.88 µs (max.) → (16.542) µs (min.), 20.678 µs (typ.), (26.88) µs (max.) : Remarks: 48.363 kHz (typ.) → 48.360 kHz (typ.) - Vertical – Cycle: 15.38 ms (min.), 16.666 ms (typ.), 18.18 ms (max.) → (14.29) ms (min.), 16.67 ms (typ.), (20.00) ms (max.) : 780 H (min.) → 778 H (min.)

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REVISION HISTORY

Edition	Document number	Prepared date	Revision contents and signature
2nd edition	DOD-PP-1449	Jun. 15, 2012	**Revision contents** P24 OPTICS • Optical characteristics • Luminance uniformity: 1.4 (typ.), 1.5 (max.) → 1.25 (typ.), 1.4 (max.) • Viewing angle - θR, θL, θU, θD: - ° (min.) → (70) ° (min.) • Note2 - Display mode: XGA → FWXGA - Horizontal cycle: 1/48.363kHz →1/48.360kHz P27 RELIABILITY TESTS • High temperature and humidity: ①50 ± 2°C, RH= 80% → 60 ± 2°C, RH= 90% • Heat cycle (Operation) (addition) • Thermal shock: ①60 ± 3°C 30minutes → 80 ± 3°C 30minutes • Dust (Operation) (addition) • Vibration, Mechanical shock - judgment: No physical damages (addition) P30-31 OUTLINE DRAWINGS • FRONT VIEW (revised) • REAR VIEW (addition) **Write** *Approved by* T. OGAWA *Checked by* ——— *Prepared by* E. YOSHIMURA
3rd edition	DOD-PP-1505	Oct. 26, 2012	**Revision contents** P5 GENERAL SPECIFICATIONS • Weight: TBD g (typ.) → (800) g (typ.) • Response time: Ton+Toff 8 ms (typ.) → (18) ms (typ.) • Backlight: Lamp holder set: Type No. TBD → Type No. 150LHS201 • Power consumption: TBD W (typ.) → (11.8) W (typ.) P7 MECHANICAL SPECIFICATIONS • Weight: TBD g → (800) (typ.), (880) (max.) g P8 LCD panel signal processing board • Power supply current: (700) (typ.), TBD (max.) mA → (480) (typ.), (900) (max.) mA P8, 9 Backlight lamp • Power supply current: - (typ.), TBD (max.) mA → (850) (typ.), (1,100) (max.) mA • Input voltage for PWM signal - High: 0.4 (max.) V → (0.35) (max.) V • PWM frequency: 10k Hz (max.) → 1k Hz (max.) • PWM duty cycle (addition) • PWM pulse width: TBD µs (min.) → 10 µs (min.) • Note6, 7 (addition) P9 Fuse (specified) P10 LED driver board (Revised) P12 Backlight lamp • Adaptable plug: or equivalent (addition) P13 Input data signal: 8bit, MAP A (MSL: High, FRC: Low or Open) (Revised, change of expression) • THC63LVDF84B → DS90C383 (change) P14 Input data signal: 8bit, MAP B (MSL: Low or Open, FRC: Low or Open) (Revised, change of expression) • THC63LVDF84B → DS90C383 (change) P15 Input data signal: 6bit (MSL: High, FRC: High) (Revised, change of expression) • THC63LVDF84B → DS90C383 (change) P13-15 Connection between receiver and transmitter for LVDS • Note1: THC63LVDM83D (THine Electronics Inc.) → DS90C383 (Texas Instruments) • Note4: TC4, TC5, TD6 → TXIN23, TXIN24, TXIN25

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REVISION HISTORY

Edition	Document number	Prepared date	Revision contents and signature
3rd edition	DOD-PP-1505	Oct. 26, 2012	**Revision contents** P24 OPTICS - Optical characteristics • Response time - Ton: 3 (typ.), TBD (max.) ms → (3) (typ.), (5) (max.) ms - Toff: 5 (typ.), TBD (max.) ms → (15) (typ.), (21) (max.) ms - Ton+Toff: 8 (typ.), TBD (max.) ms → (18) (typ.), (26) (max.) ms **Signature of writer** *Approved by* K. Fujimoto — K. FUJIMOTO *Checked by* — *Prepared by* E. Yoshimura — E. YOSHIMURA

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